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PATENT ABSTRACTS OF JAPAN

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(54) FAILURE INFORMATION GATHERING SYSTEM OF MULTIPROCESSOR SYSTEM

(57) Abstract:

PURPOSE: To eliminate the need to connect a simulator for information gathering by gathering failure information automatically by a master processor according to memory map information and storing the gathered information.

CONSTITUTION: The master processor 110 sends a failure information gathering indication to a slave processor 120 which gets failed through a bus 130 according to the memory map information 180 when the failure of a slave processor 120 is detected. The slave processor 120 sends the information which is gathered according to the failure information indication to the master processor 110 through the bus 130 according to the reception of a sent

signal. Further, the master processor 110 stores the failure information which is received from the slave processor 120 in a trouble information storage means 140 and reads the stored failure information out. Consequently, a person in charge of maintenance need not perform operation for gathering information at each time, not to mention the elimination of the need for the connection of the simulator and failure information can be gathered automatically.

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